

REMARKS

The Office Action dated April 3, 2008 has been reviewed and carefully considered. Claims 1-15 remain pending. Claim 10 has been amended to become an independent claim. Consequently the current independent claims are 1, 10 and 14. Reconsideration of the above-identified application, as amended and in view of the following remarks, is respectfully requested.

As an initial matter, the Examiner has requested clarification of the fact that previously, claim 1 had been designated as amended, yet contained no indication as where and/or what had been amended. Applicants regret this error. Previously, claim 1 had not been amended from its originally filed form. With the current amendment, claim 1 stands "currently amended" and is properly designated as such.

In paragraph 5 of the Office Action, claims 1-15 are objected to because of various informalities. In accordance with the Examiner's suggestions, Applicants have deleted parenthetical reference characters previously contained in the claims. With this amendment, Applicants believe that the reason for the Examiner's objection to these claims has been overcome. Applicants respectfully request the objection be withdrawn.

Applicants note with appreciation the indication that Claims 4-12 and 15 would be allowable if rewritten so as not to depend from a rejected claim, and with no change in scope. Claim 10 has been so rewritten, and is now believed to be in condition for

allowance. The remaining claims have not been so rewritten because, for the reasons given below, their base claim, as amended, is believed to be allowable.

Claims 1-3, 13 and 14 stand rejected under 35 USC 102(b) as being anticipated by Dawson et al., U.S. Patent No. 6,229,506.

Applicant respectfully disagrees with, and explicitly traverses, the Examiner's reason for rejecting these claims.

Claim 1, as amended, recites:

1. An active matrix display device comprising an array of display pixels, each pixel comprising:
 - a current driven light emitting display element and a first drive transistor for driving a current through the display element, the display element and the first drive transistor being in series between power supply lines;
 - a first storage capacitor for storing a gate-source voltage of the first drive transistor;
 - a second drive transistor, operable only during pixel programming, for providing a drive current based on an input voltage provided to the gate of the second drive transistor; and
 - a second storage capacitor for storing the input voltage for driving the second drive transistor.

Dawson et al. teaches an active matrix light emitting diode pixel structure and concomitant method. Paragraph 5 of the Office Action submits that the second drive transistor of the claimed invention (an example of which is illustrated as item 30 in Fig. 3 of the present application) is taught by item 365 of Dawson's Fig. 3. Claim 1, as

amended, recites that the second drive transistor is operable only during pixel programming. Support for this feature is found, *inter alia*, in the published specification at paragraph [0055].

As depicted in Fig. 3 of Dawson, P1 (item 365) is connected in series with transistor P2 (item 375) and OI.ED (item 380). Moreover as described at col. 5, lines 28-30: "The transistor 365 behaves like a constant current source." Accordingly, Applicants submit that Dawson teaches away from the above described feature of the present invention wherein the second drive transistor is operable only during pixel programming.

A claim is anticipated only if each and every element recited therein is expressly or inherently described in a single prior art reference. Dawson cannot be said to anticipate claim 1 of the present invention, because Dawson fails to disclose each and every element recited. As shown, Dawson fails to disclose the limitation of "a second drive transistor, operable only during pixel programming, for providing a drive current based on an input voltage provided to the gate of the second drive transistor" as is recited in claim 1.

Having shown that Dawson fails to disclose each and every element claimed, Applicants submit that the reason for the Examiner's rejection of claim 1 has been overcome and can no longer be sustained. Applicant respectfully requests reconsideration, withdrawal of the rejection and allowance of claim 1.

With regard to claim 14 of the present invention, Applicants submit that the feature "switching off the second drive transistor" when taken in conjunction with the claim's preamble recitation of "a method of addressing an active matrix display device" is comparable to the above described analysis of amended claim 1. That is, the second drive transistor is only operable during the addressing phase (or "programming" phase as defined in paragraph [0053]). Consequently, for at least the same reasons given above with respect to claim 1, Applicants submit that the reason for the Examiner's rejection of claim 14 has been overcome and can no longer be sustained. Applicant respectfully requests reconsideration, withdrawal of the rejection and allowance of claim 14.

With regard to claims 2-9, 11-13 and 15, these claims ultimately depend from one of the independent claims, which have been shown to be not anticipated and allowable in view of the cited references. Accordingly, claims 2-9, 11-13 and 15 are also allowable by virtue of their dependence from an allowable base claim.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. A Notice of Allowance is respectfully requested.

Respectfully submitted,

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